ECE 3421 VLSI Design and Simulation
Spring 2013

Class Meeting Time:
TuTh 3:30-4:20pm, ITEB 125 (Lectures)
W 2-4pm, ITEB C27 (Labs)

Instructor:
Prof. Omer Khan
Office: ITEB 439
Email: khan@uconn.edu
Office Hours: Thursday 1:00-2:00
Course website: http://www.engr.uconn.edu/~omer.khan/courses/ece3421_s13/index.html

TAs for ECE 3421:

Qingchuan Shi (Labs)
Email: qingchuan.shi@uconn.edu
Office Hours: TBD, ITEB 314

Farrukh Hijaz (Homeworks and Exams)
Email: farrukh.hijaz@uconn.edu
Office Hours: TBD, ITEB 314

Textbook:

CMOS VLSI Design: A Circuits and Systems Perspective, 3rd Edition or 4th Edition, Neil
Weste, David Harris, Addison Wesley, 2003/2010

Grading Policy:

Three Exams	40%
Homework	10%
Class Participation	5%
Labs	25%
Project	20%

First two exams tentatively scheduled for February 28th and April 11th and the third exam will be
in the finals week. There will be 4-6 homework assignments, 5 lab assignments and a project.

Rules:
Participation in class discussion is strongly encouraged.
Participation in lab sessions is *required.*
Homework is due at the beginning of class on the due day